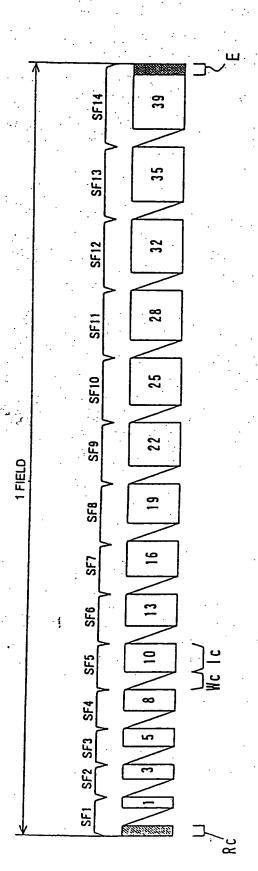
E. C. I

SELECTIVE ERASURE



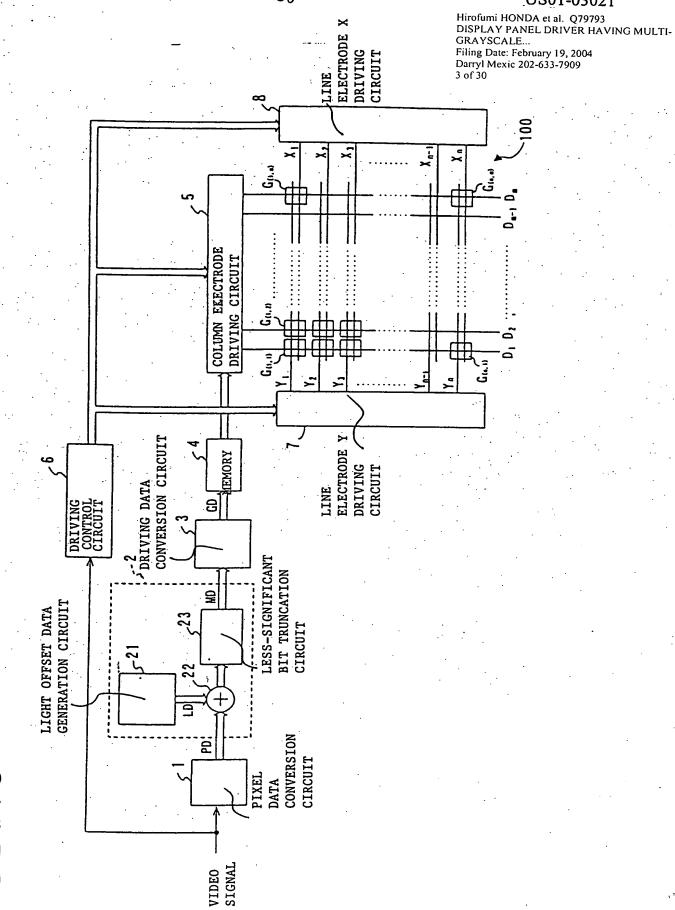
Hirofumi HONDA et al. Q79793
DISPLAY PANEL DRIVER HAVING MULTIGRAYSCALE...
Filing Date: February 19, 2004
Darryl Mexic 202-633-7909
1 of 30

Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 2 of 30

LIGHT EMISSION INTENȘITY SF SF 13 SF 0 0 SF 0.000 LIGHT EMISSION DRIVING PATTERN IN A FIELD SF R P ۳ SF Ŗ 0 0 0 SF SF بر بر SF SF 0.0 · O 모 . 1 မှ S SELECTIVE ERASURE

Ds

BLACK DOT: SELECTIVE ERASURE WHITE DOT: LIGHT EMISSION



FIG

Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 4 of 30

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FIG. 4

●:ERASURE ADDRESSING DISCHARGE O:SUSTAIN DISCHARGE LIGHT EMISSION

Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 5 of 30

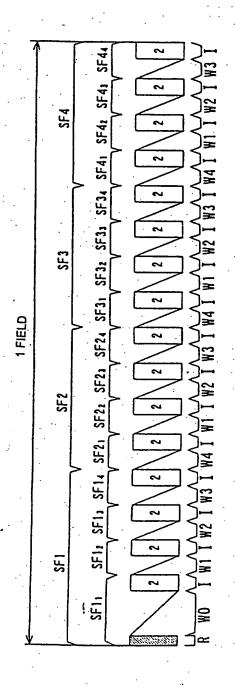


FIG. 5

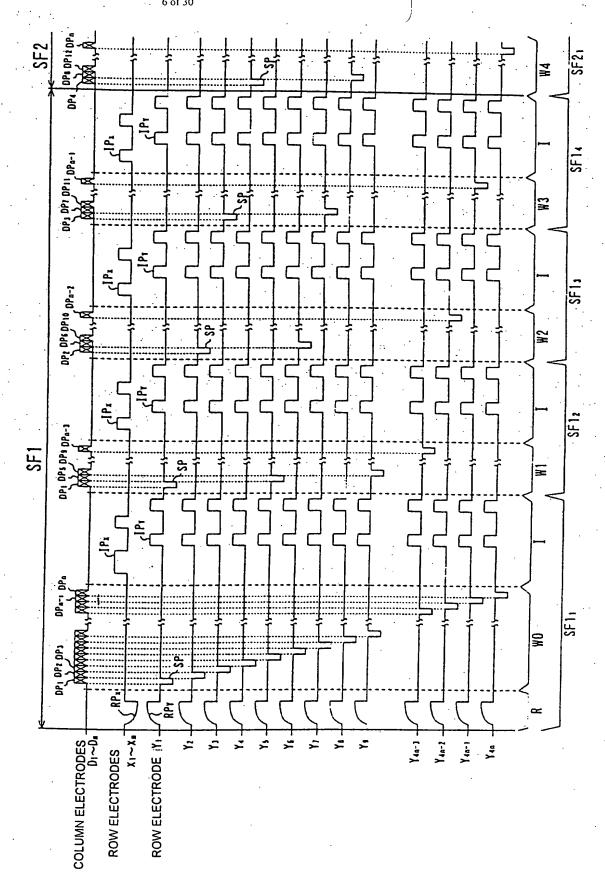
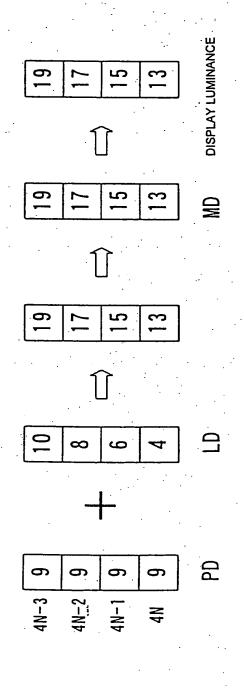
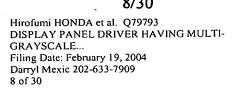


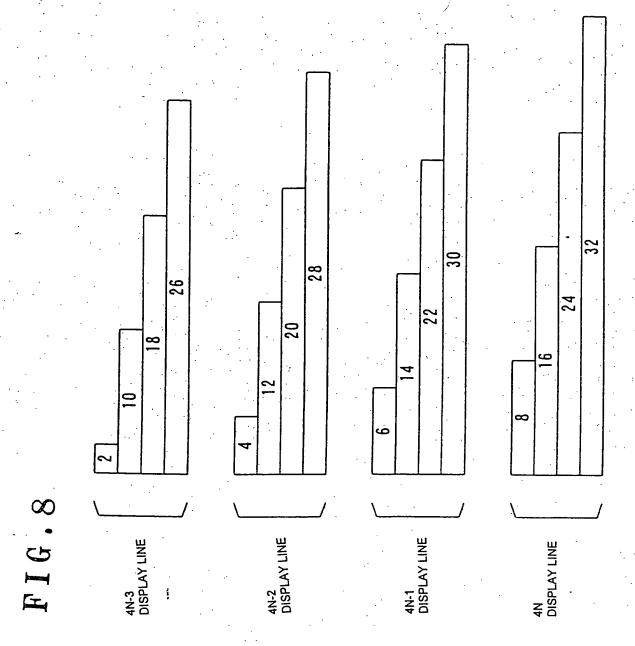
FIG. 6

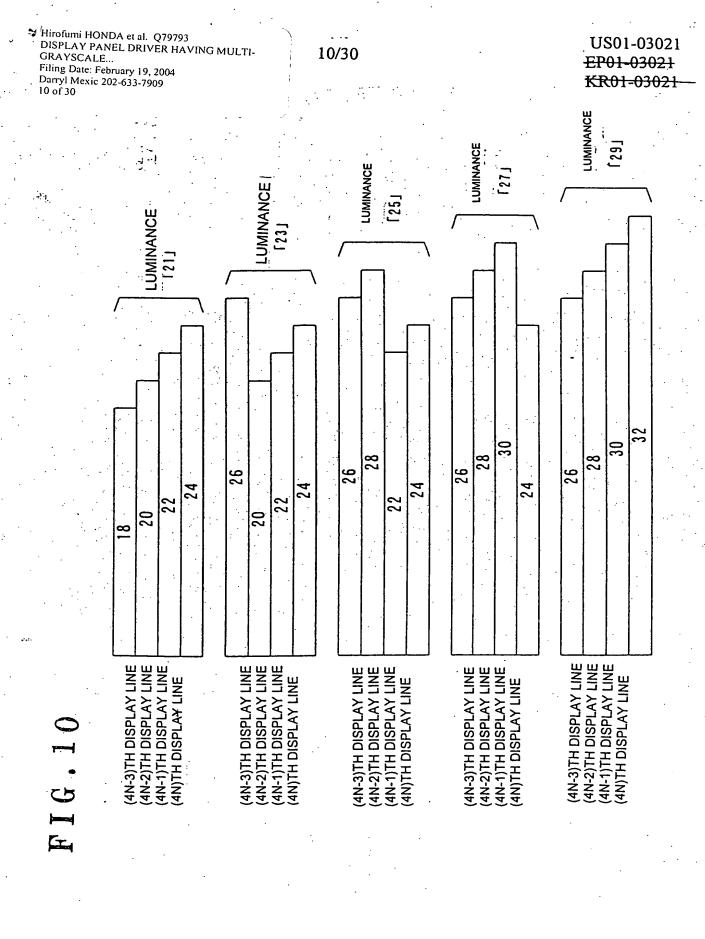
Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 7 of 30





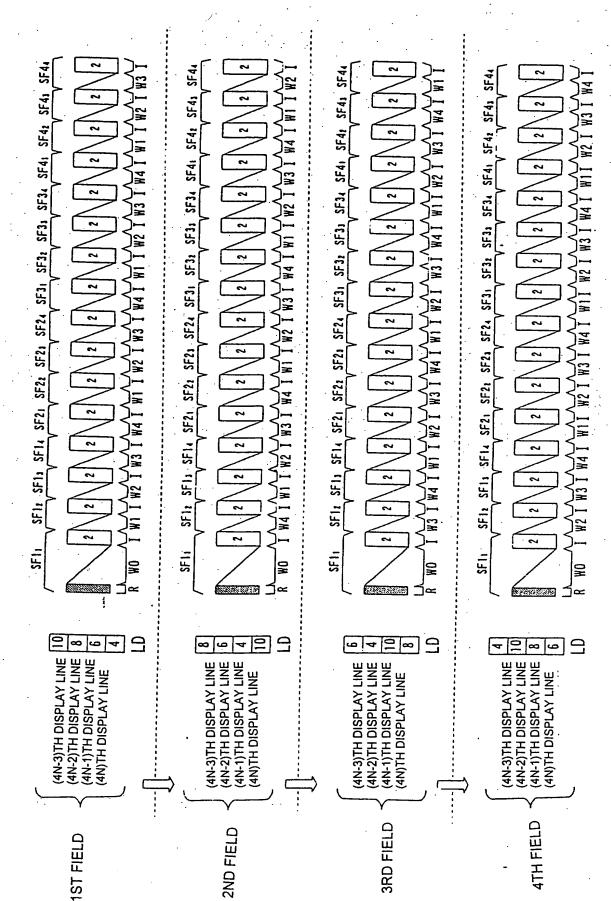
US01-03021 -EP01-03021 KR01-03021



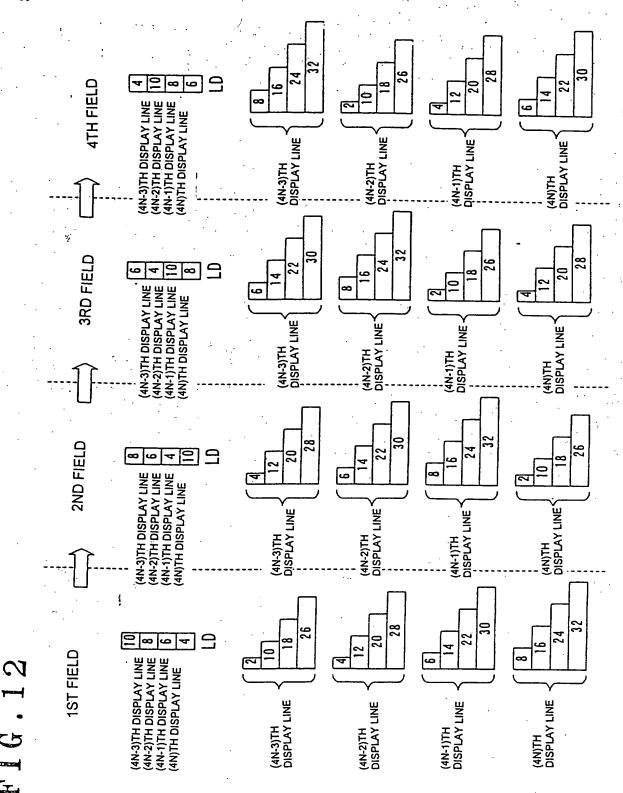


Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 11 of 30

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Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 12 of 30



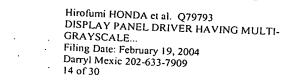
ELEGTRODE X DRIVING CIRCUIT 13/30 LINE 80 COLUMN ELECTRODE DRIVING CIRCUIT و ق CONVERSION CIRCUIT ELECTRODE Y MEMORY 9 DRIVING CIRCUIT .-20 DRIVING DATA DRIVING CONTROL CIRCUIT ප LESS-SIGNIFICANT BIT TRUNCATION CIRCULT ş GENERATION CIRCUIT 200 < 230 LINE OFFSET DATA CIRCUIT DITHER MATRIX ē FIRST DATA CONVERSION CIRCUIT PIXEL DATA CONVERSION 2 CIRCUIT VIDEO SIGNAL

Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-

GRAYSCALE...

Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 13 of 30

FIG. 13



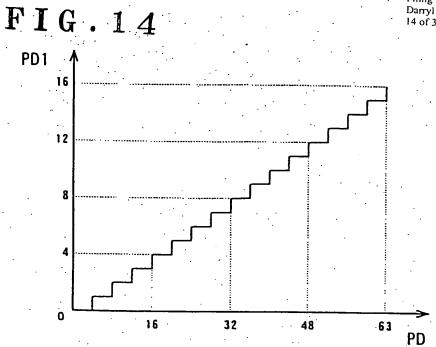
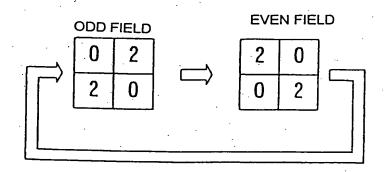


FIG.15

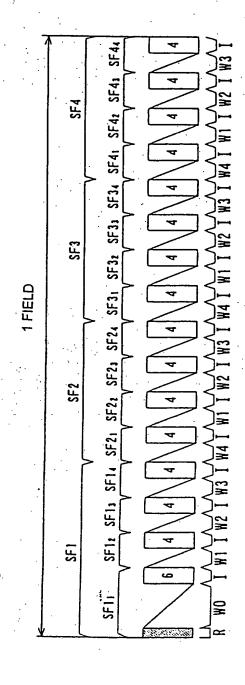


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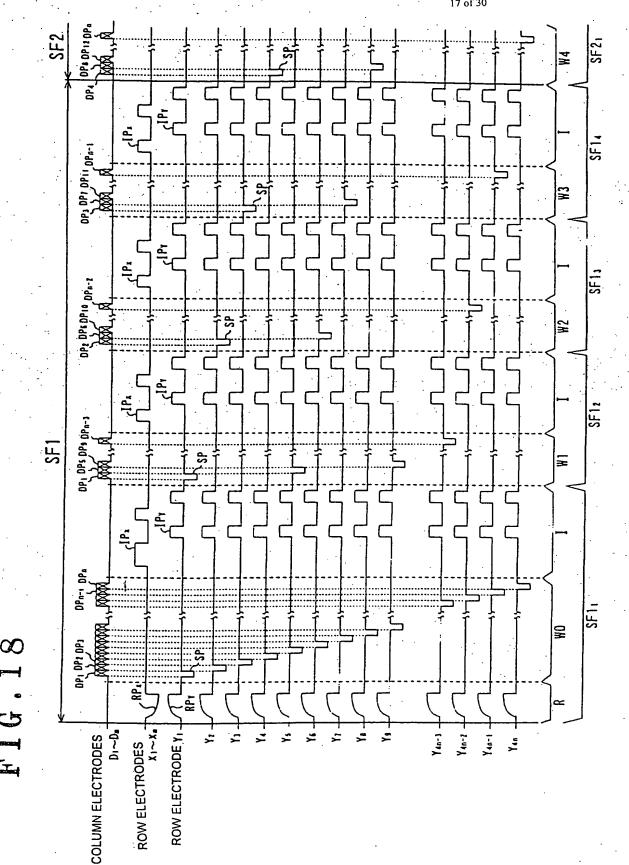
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●:ERASURE ADDRESSING DISCHARGE O:SUSTAIN DISCHARGE LIGHT EMISSION

Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 16 of 30



Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 17 of 30



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Hirofumi HONDA et al. Q79793
DISPLAY PANEL DRIVER HAVING MULTIGRAYSCALE...
Filing Date: February 19, 2004
Darryl Mexic 202-633-7909
18 of 30

Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 19 of 30

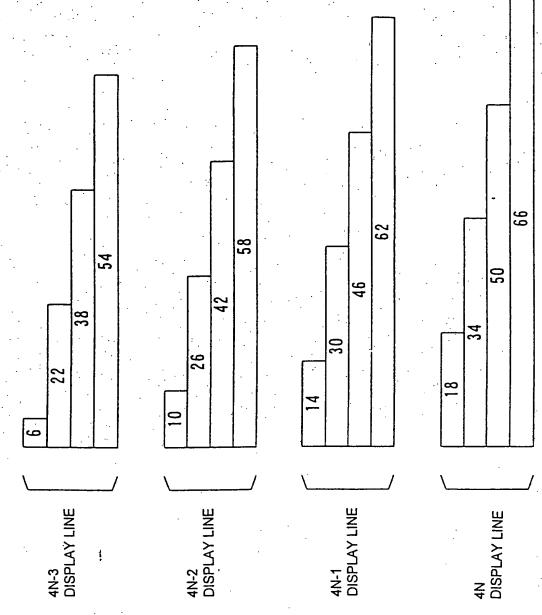


FIG. 20

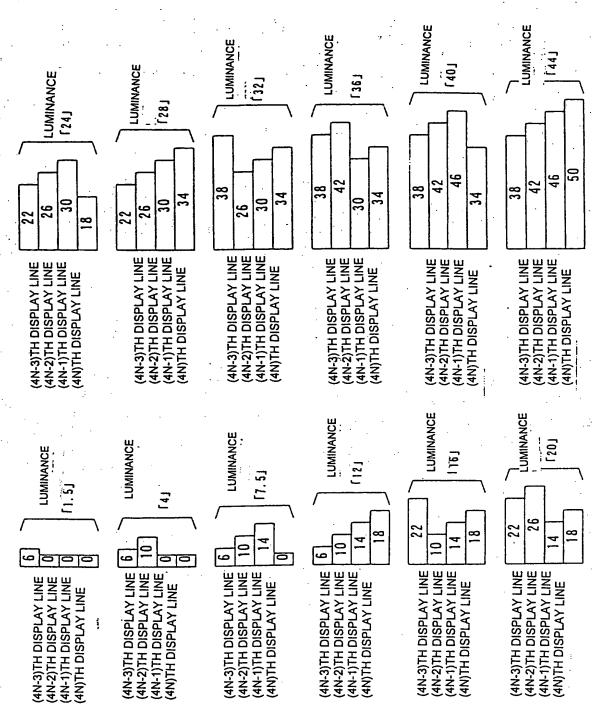
FIG. 21

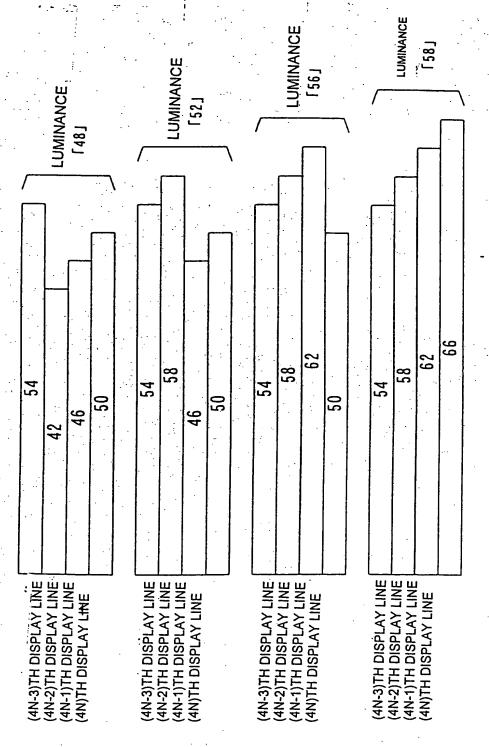
Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-

Filing Date: February 19, 2004 Darryl Mexic 202-633-7909

20 of 30

GRAYSCALE...





Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 21 of 30

Hirofumi HONDA et al. Q79793
DISPLAY PANEL DRIVER HAVING MULTIGRAYSCALE...
Filing Date: February 19, 2004
Darryl Mexic 202-633-7909
22 of 30

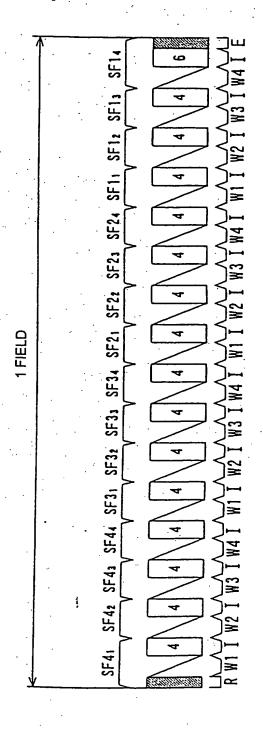


FIG. 23

Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 23 of 30

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Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 24 of 30

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Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 25 of 30

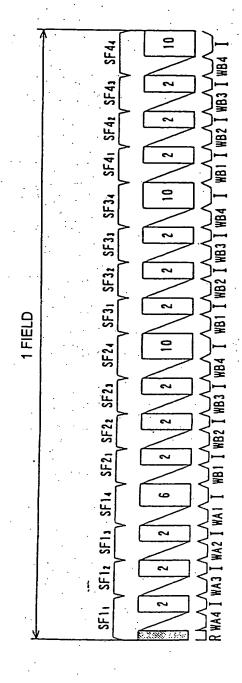
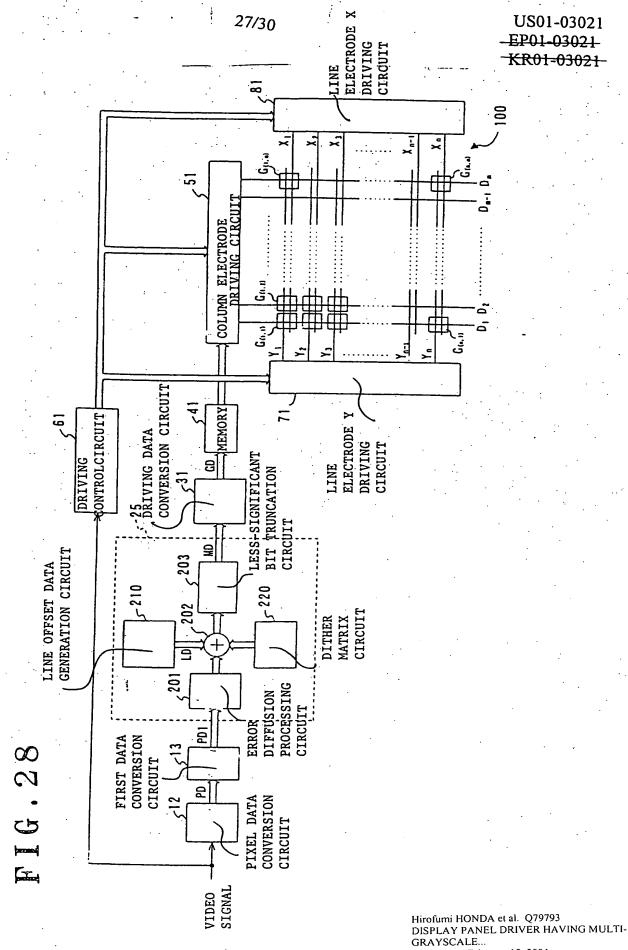


FIG. 26

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◎ : WRITING ADDRESSING DISCHARGE●:ERASURE ADDRESSING DISCHARGE○:SUSTAIN DISCHARGE LIGHT EMISSION



Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 27 of 30

FIG.29

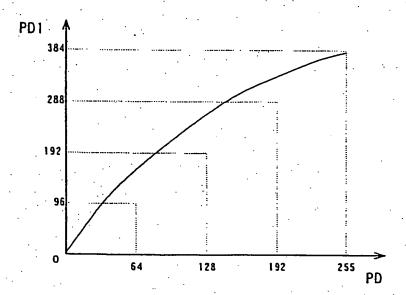


FIG.30

(8N-7)TH DISPLAY LINE	0
(8N-6)TH DISPLAY LINE	4
(8N-5)TH DISPLAY LINE	8
(8N-4)TH DISPLAY LINE	12
(8N-3)TH DISPLAY LINE	16
	20
(8N-1)TH DISPLAY LINE	24
(8N)TH DISPLAY LINE	28

Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909 29 of 30

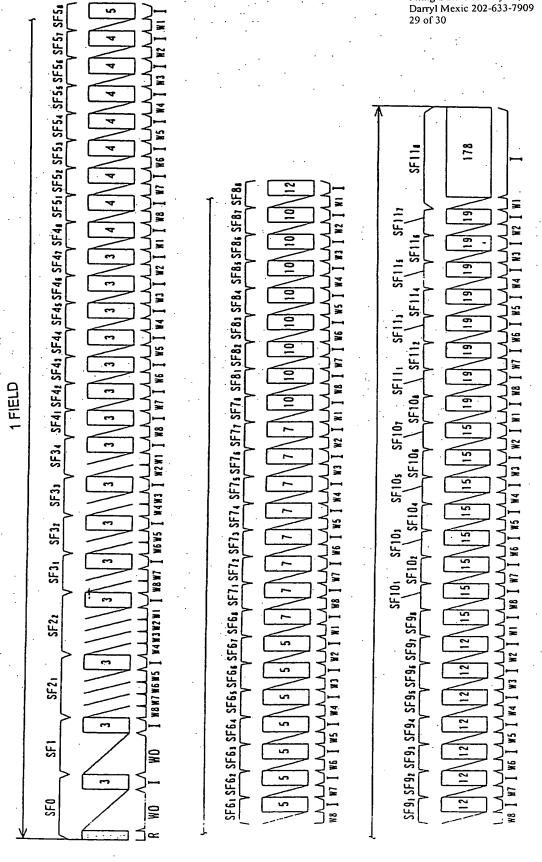


FIG. 32

Hirofumi HONDA et al. Q79793 DISPLAY PANEL DRIVER HAVING MULTI-GRAYSCALE... Filing Date: February 19, 2004 Darryl Mexic 202-633-7909

30 of 30

